

### PATENT APPLICATION

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Hideharu ITATANI et al.

Group Art Unit: 2895

Application No.:

10/521,248

Docket No.: 122430

Filed: April 11, 2005

For:

METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND

SUBSTRATE PROCESSING APPARATUS

## REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

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Date: February 27, 2009

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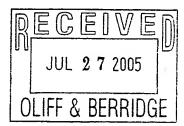
# United States Patent And I

UNITED STATES DEPARTMENT OF COMMERCE UNITED STATES DEPARTMENT OF COMMI United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.mpto.gov

**FILING OR 371** DRAWINGS TOT CLMS IND CLMS FIL FEE REC'D ATTY.DOCKET NO ART UNIT APPL NO. (c) DATE 20 3 122430 04/11/2005 2812 1030 10/521,248

**CONFIRMATION NO. 9181** 

25944 OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320



**FILING RECEIPT** OC000000016561538\*

Date Mailed: 07/22/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

#### Applicant(s)

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#### Assignment For Published Patent Application

HITACHI KOKUSAI ELECTRIC INC., TOKYO, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

#### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/08964 07/15/2003

#### Foreign Applications

JAPAN 2002-205627 07/15/2002

Projected Publication Date: 10/27/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method for manufacturing semiconductor device and substrate processing apparatus

**Preliminary Class** 

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